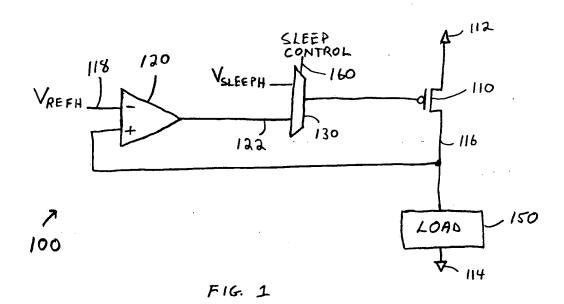
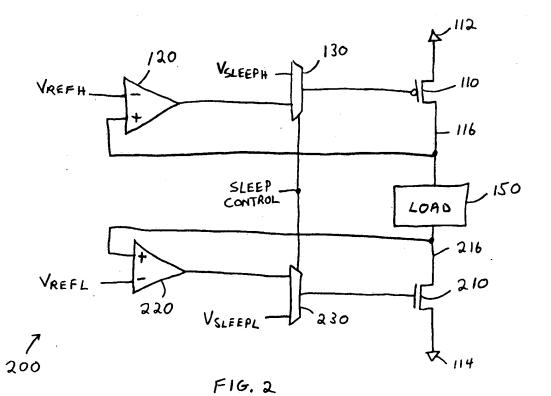
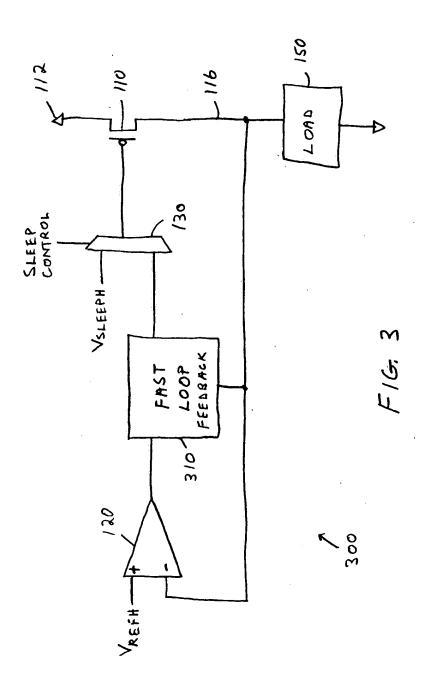
INVENTORS NAME: Peter Hazucha et al. DOCKET No.: 80107.040US1

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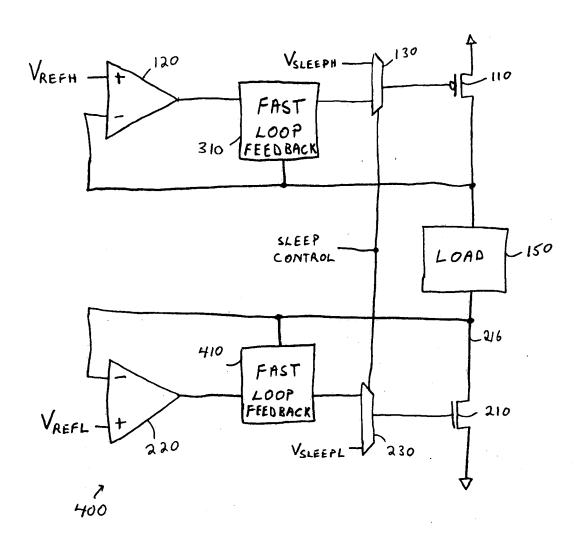




TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al. DOCKET No.: 80107.040US1

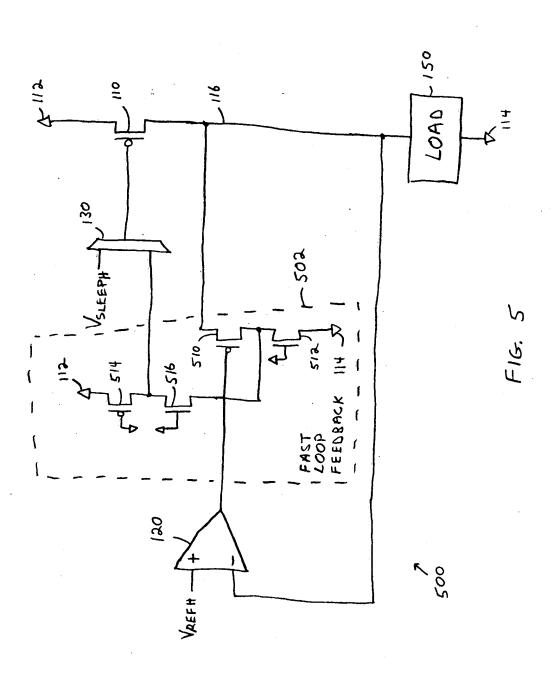


INVENTORS NAME: Peter Hazucha et al. DOCKET No.: 80107.040US1

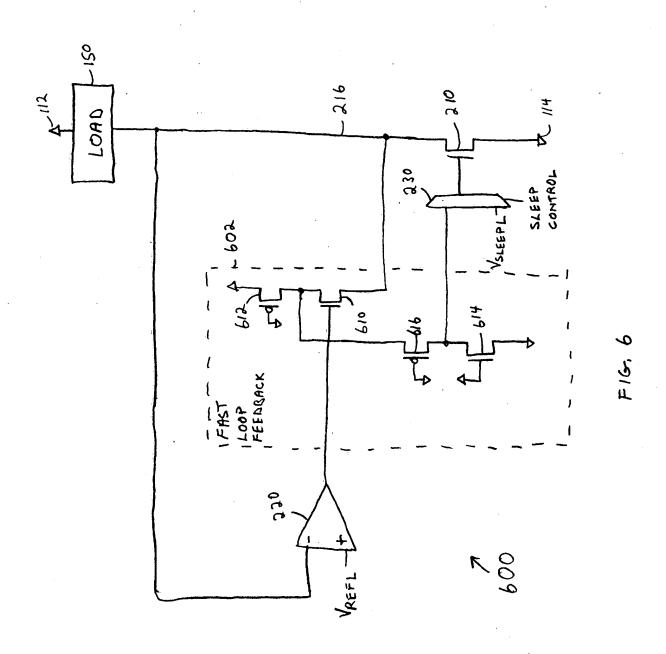


F1G. 4

TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al. DOCKET No.: 80107.040US1

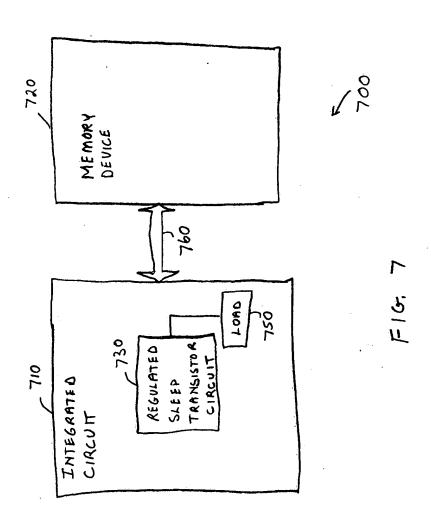


TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al. DOCKET No.: 80107.040US1



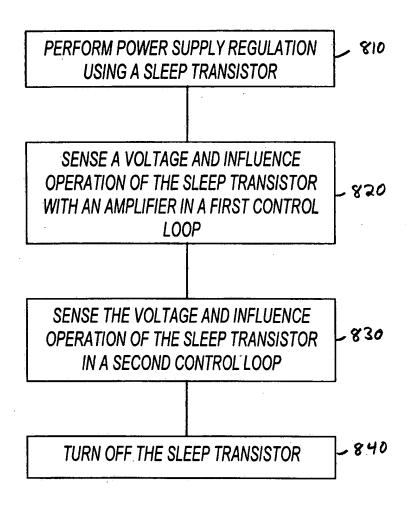
TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al.

DOCKET No.: 80107.040US1



INVENTORS NAME: Peter Hazucha et al. DOCKET No.: 80107.040US1

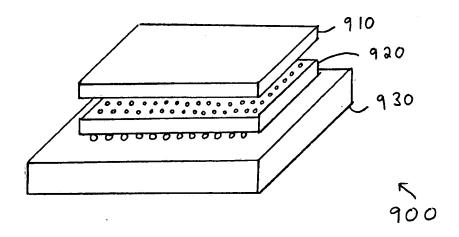
7./8



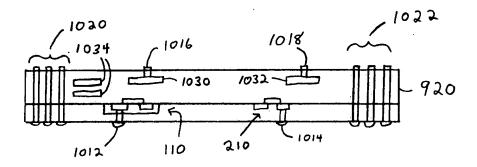
800

FIG. 8

INVENTORS NAME: Peter Hazucha et al. DOCKET No.: 80107.040US1



F16: 9



F1G. 10